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Note: There may be some changes between versions. So, your software may look a bit different from this manual.

Quick Guide

Select menu File – Browse 1st file..., and choose the 1^{st} file to be compared. See screenshot below:



Then select menu File - Browse 2nd file..., and choose the 2^{nd} file to be compared. See screenshot below:



Now, select menu Compare – Execute to run. See screenshot below:

🙀 Compare						
File	Compare Log	Advanced	Help			
	Options					
	Execute					

Compare Type

Auto

File extension	Compare Type	Comments
.ucf	Xilinx ucf	
.lci	Lattice lci	
.qsf	Altera .qsf	
.zip	Zip Compressed	
.xls	BOM	
	Netlist	Ignore file extension
	Package	Ignore file extension
	Position	Ignore file extension
	BOM	

If 'Compare Type' not set, the software will automatically try to compare as below orders by checking file extension:

Set

The 'Compare Type' could be set from menu **Compare – Options...** See screenshot below:



Supported types are:

Option 💌			
	Compare Type	Auto 💌	
Keyword Case		Auto BOM Package Netlist	
	ОК	ucf lai	
		qsf Position Zip	

If 'Compare Type' is set, the software will try just this type only.

BOM File Comparing

If two BOM files are compared, the 'BOM Compare Setting' dialog will popup.

The software will select auto detected field. Please choose yours if it does not match your need.

Notes

About RenameFix

Background

When OrCAD Capture is generating a netlist, Capture will add a postfix to a netlist to make sure that the name is unique. The generated postfix is random.

The designer may rename a netlist without touching the connections.

Example

Supposed there are two pages in the schematic, and there is a signal called TDI. But the two TDI signals don't have any connection between them, e.g. there is no Off-Page Connector or Hierarchy Port for them.

Capture will generate a netlist like this:

```
...
TDI; ....
TDI_1234567; .....
```

. . .

That's to say, one signal is kept, and the other will be added with a postfix to distinguish them, to split them from connected together.

But, the postfix is random when you generate the netlist.

If you are importing netlist to Allegro, the PCB trace of renamed netlist will not be lost, because the connection is unchanged. But the property will lose.

For example, if you set impedance of TDI before. The netlist is renamed to TDI_1234567, the attached property will disappear.

We suggest you to name the netlist in a better way. For example, you should not name all TDI of difference part with the same alias TDI, but you should name them to TDI_FPGA, TDI_CPLD and TDI_CPU.

Please note this suggestion does not work for a backplane design.

What' more, we noticed this even off-page connector is use.

For example, the netlist is TEST; R1.1 R2.1 once, but it became TEST_11223344; R1.1 R2.1 in another time.

What the Software Do

The software will compare the two netlist files, and get all connections for each file. The software will try to replace netlist with last known to compare the connection.

How to Use

After you have run the comparison, select menu **Advanced** –**Rename Fix File n** (n could be 1 or 2).

Please set the new netlist filename. The default new file name is *Original_Filename_*RenameFix.*net*. For example, supposed original filename is D:\Test1.net, default filename for new file will be D:\Test1_RenameFix.net. The software will do Search & Replace automatically.

Please use new created file to run the comparison again and you'll get more helpful result.

BOM File Format Requirements

Excel 2000 files are supported. But plain text files exported from Excel are strongly recommended. Please note no <Alt>+<Enter> new line character should be used in any Excel cell. Capture exported BOM files are supported.

The 1st line of BOM file must be headers. Extra lines should be removed.

Keyword (Part Number, usually) should not be empty. Fill 'NA' if it is not defined.

Use comma (,) to split multi part references.

Date	Version	Author	Changes
2019/9/19			Add 'Compare Type' order and setting instruction;
2014/9/11			Add 'BOM File Format Requirement';
2014/6/9			Add line above footer;
2013/7/29			First release;

Revision History